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Chip Design for Submicron VLSIChip Design For Submicron Vlsi: Cmos Layout & SimulationThe Electrical Engineering HandbookLow-Power Electronics DesignVLSI DesignMOSFET Technologies for Double-Pole Four-Throw Radio-Frequency SwitchNoise Modeling, Evaluation and Noise-tolerant Design of Very Deep Submicron VLSI CircuitsElectron-beam, X-ray, & Ion-beam Techniques for Submicrometer Lithographies VTIming Analysis of CMOS Logic Gates in Deep Submicron VLSI DesignIntegrierte Digitale SchaltungenBiCMOS Digital ICChemical AbstractsMissions, Technologies, and Design of Planetary Mobile VehiclesElectrical Overstress/Electrostatic Discharge Symposium Proceedings1991 International Symposium on VLSI Technology, Systems and ApplicationsHigh-Speed Semiconductor DevicesIndex to IEEE PublicationsProceedings of the ... Midwest Symposium on Circuits and SystemsAsian Test SymposiumThe Science and Engineering of Microelectronic Fabrication John Paul Uyemura Umura Wai Kai Chen Christian Piguet Viranjay M. Srivastava Li Ding Phillip D. Blais Xueping Jiang Heinrich Klar James B. Kuo Centre national d'®tudes spatiales (France) S. M. Sze Institute of Electrical and Electronics Engineers Stephen A. Campbell

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and Electronics Engineers Stephen A. Campbell

this book teaches the principles of physical design layout and simulation of cmos integrated circuits it is written around a very powerful cad program called microwind that is available on the accompanying cd rom featuring a friendly interface microwind is both educational and useful for designing cmos chips

the electrical engineer s handbook is an invaluable reference source for all practicing electrical engineers and students encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students this text will most likely be the engineer s first choice in looking for a solution extensive complete references to other sources are provided throughout no other book has the breadth and depth of coverage available here this is a must have for all practitioners and students the electrical engineer s handbook provides the most up to date information in circuits and networks electric power systems electronics computer aided design and optimization vlsi systems signal processing digital systems and computer engineering digital communication and communication networks electromagnetics and control and systems about the editor in chief wai kai chen is professor and head emeritus of the department of electrical engineering and computer science at the university of illinois at chicago he has extensive experience in education and industry and is very active professionally in the fields of circuits and systems he was editor in chief of the ieee transactions on circuits and systems series i and ii president of the ieee circuits and systems society and is the founding editor and editor in chief of the journal of circuits systems and computers he is the recipient of the golden jubilee medal the education award and the meritorious service award from the ieee circuits and systems society and the third millennium medal from the ieee professor chen is a fellow of the ieee and the american association for the advancement of science 77 chapters encompass the entire field of electrical engineering thousands of valuable figures tables formulas and definitions extensive bibliographic references

the power consumption of integrated circuits is one of the most problematic considerations affecting the design of high performance chips and portable devices the study of power saving design methodologies now must also include subjects such as systems on chips embedded software and the future of microelectronics low power electronics design covers all major aspects of low power design of ics in deep submicron technologies and addresses emerging topics related to future design this volume explores in individual chapters written

by expert authors the many low power techniques born during the past decade it also discusses the many different domains and disciplines that impact power consumption including processors complex circuits software cad tools and energy sources and management the authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality they investigate nanotechnologies optical circuits ad hoc networks e textiles as well as human powered sources of energy low power electronics design delivers a complete picture of today s methods for reducing power and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now

this book provides analysis and discusses the design of various mosfet technologies which are used for the design of double pole four throw dp4t rf switches for next generation communication systems the authors discuss the design of the dp4t rf switch by using the double gate dg mosfet as well as the cylindrical surrounding double gate csdg mosfet the effect of hfo2 high dielectric material in the design of dg mosfet and csdg mosfet is also explored coverage includes comparison of single gate mosfet and double gate mosfet switching parameters as well as testing of mosfets parameters using image acquisition

diese dritte auflage bietet einen vollst ndig überarbeiteten blick auf alle aspekte der integrierten digitalen schaltungstechnik aufbauend auf der halbleiterphysik moderner cmos transistoren und deren herstellung werden digitale und wenn nötig analoge grundschaltungen behandelt weiter werden funktionsblöcke wie speicherschaltungen addierer und multiplizierer besprochen als neuer schwerpunkt der dritten auflage ziehen sich die auswirkungen der zeitlichen und örtlichen schwankungen der versorgungsspannungen und der temperatur sowie die einflüsse der herstellungsbedingten schwankungen der transistorparameter wie ein roter faden durch alle kapitel methoden zur minimierung der verlustleistung bilden einen weiteren neuen schwerpunkt des buches es wird wie in den bisherigen auflagen die technologieorientierte und die systemorientierte schaltungstechnik gemeinsam und zusammenh ngend dargestellt

introduces the physical principles and operational characteristics of high speed semiconductor devices intended for use by advanced students as well as professional engineers and scientists involved in semiconductor device research it includes the most advanced and important topics in high speed semiconductor devices initial chapters cover material properties advanced technologies and novel device building blocks and serve as the

basis for understanding and analyzing devices in subsequent chapters the following chapters cover a group of closely related devices that includes mosfets mesfets heterojunction fets and permeable base transistors hot electron transistors microwave diodes and photonic devices among others each chapter is self contained and features a summary section a discussion of future device trend and an instructional problem set

the science and engineering of microelectronic fabrication provides an introduction to microelectronic processing geared towards a wide audience it may be used as a textbook for both first year graduate and upper level undergraduate courses and as a handy reference for professionals the text covers all the basic unit processes used to fabricate integrated circuits including photolithography plasma and reactive ion etching ion implantation diffusion oxidation evaporation vapor phase epitaxial growth sputtering and chemical vapor deposition advanced processing topics such as rapid thermal processing nonoptical lithography molecular beam epitaxy and metal organic chemical vapor deposition are also presented the physics and chemistry of each process is introduced along with descriptions of the equipment used for the manufacturing of integrated circuits the text also discusses the integration of these processes into common technologies such as cmos double poly bipolar and gaas mesfets complexity performance tradeoffs are evaluated along with a description of the current state of the art devices each chapter includes sample problems with solutions the book also makes use of the process simulation package suprem to demonstrate impurity profiles of practical interest

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